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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known

Application Number	10/771944
Filing Date	020404
First Named Inventor	Kevin Lindell
Art Unit	3662
Examiner Name	GREGORY
Attorney Docket Number	312-022

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Joy		US- 6,529,712-B1	03-04-2003	William J. Domino	
Doy		US- 6,487,264-B1	11-26-2002	Daniel Alley	
Doy		US- 6,011,439 - A	01-04-2000	Vincent Colarossi	
Doy		US- 5,625,327 - A	04-29-1997	Gary T. Carroll	
Doy		US- 5,603,111 - A	02-11-1997	Michael A. Wyatt	
Doy		US- 5,453,748 - A	09-26-1995	Kevin W. Lindell	
Doy		US- 4,356,456 - A	10-26-1982	Vazil Uzunoglu	
Doy		US- 4,355,404 - A	10-19-1982	Vazil Uzunoglu	
Joy		US- 4,274,067 - A	06-16-1981	Vazil Uzunoglu	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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Date Considered

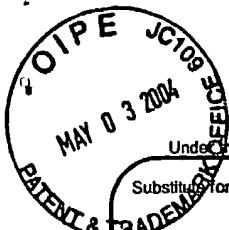
8/9/04

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Sheet

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of

1

Complete if Known

Application Number 10/771944

Filing Date 020404

First Named Inventor Kevin Lindell

Art Unit 3662

Examiner Name GREGORY

Attorney Docket Number 312-022

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JKY		SUN JONG KWON Performance Analysis of CDPD Sleep Mode for Power Conservation in Mobile End Systems, IEICE Trans. Commun., Vol. E84-B, No. 10 October 2001	
DY		Y. DEVAL HiperLAN 5.4 GHz Low Power CMOS Synchronous Oscillator 2001 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium, May 20-22, 2001 Phoenix Arizona pgs 53-56	
DY		Y. DEVAL Low-Power Synchronous Oscillator in SGe Technology for UMTS 2000 Applications MICROWAVE CONFERENCE, 2000 ASIA PACIFIC, 3-6 Dec. 2000 Sydney AU pgs. 783-787	
DCY		MARVIN TAM Theoretical Analysis of a Coherent Phase Synchronous Oscillator, IEEE Transactions on Circuits and Systems, Vol. 39, No. 1 January 1992	
DCY		VASIL UZUNOGLU Synchronous and the Coherent Phase-Locked Synchronous Oscillators, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1989	
DCY		VASIL UZUNOGLU Some Important Properties of Syncronous Oscillators, Proceedings of the IEEE, Vol. 74, No. 3, March 1986	
DCY		VASIL UZUNOGLU The Synchronous Oscillator: A Synchronization and Tracking Network, IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 6, December 1985	
JKY		ANDRE KESTELOOT A practical Direct-Sequence Spread-Spectrum UHF Link , QST Magazine, May 1989, pgs. 14-21	

Examiner Signature		Date Considered	8/9/04
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